

PATENT ABSTRACTS OF JAPAN

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(21)Application number : 06-310616 (71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

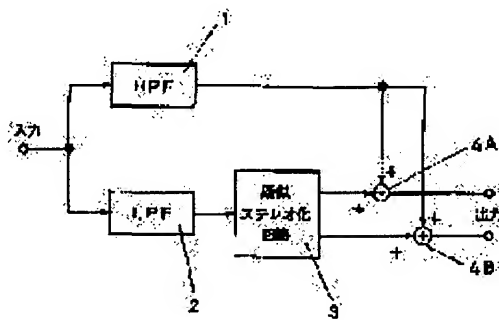
(22)Date of filing : 14.12.1994 (72)Inventor : SUZUKI TATSUYA

(54) SOUND FIELD PROCESSING CIRCUIT AND SPEAKER SYSTEM FOR REPRODUCING SOUND FIELD

(57)Abstract:

PURPOSE: To provide a sound field processing circuit and a sound field reproducing speaker system which attain a sense of spread environmental sounds and reverberation sounds as well as a sense of movement of a sound image with respect to sound field reproduction of a movie or the like.

CONSTITUTION: A monaural sound field signal is inputted to a high-pass filter 1 and a low-pass filter 2, and is divided into a high band component and a low band component. The output signal of the low-pass filter 2 is converted to pseudo stereo signals by the use of a pseudo stereo signal conversion circuit 3. The output of the high-pass filter 1 is added to two output signals of the circuit 3 by adders 4A and 4B. As the result, two signals which are monaural in the high band and are pseudo stereo in the low band are outputted from adders 4A and 4B.



LEGAL STATUS

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FILE SOUND.DOC

US AND FOREIGN EQUIVALENTS OF JP 8168100*

SYSTEM:OS - DIALOG OneSearch

File 351:Derwent WPI 1963-2003/UD,UM &UP=200335

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File 345:Inpadoc/Fam.& Legal Stat 1968-2003/UD=200321

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File 348:EUROPEAN PATENTS 1978-2003/May W04

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Set	Items	Description
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?s pn=jp 8168100

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1/7/1 (Item 1 from file: 351)

DIALOG(R)File 351:Derwent WPI

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010855816 **Image available**

WPI Acc No: 1996-352769/*199635*

Acoustic situation processing circuit for pseudo speaker system for television - has pair of adders to add output of HPF to two output signals from pseudo stereo circuit

Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 8168100	A	19960625	JP 94310616	A	19941214	199635 B

Priority Applications (No Type Date): JP 94310616 A 19941214

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 8168100	A	8	H04S-005/00	

Abstract (Basic): JP 8168100 A

The circuit has a HPF (1) and an LPF (2) which divides a monaural acoustic signal into a low frequency component and a high frequency component. The output signal of the LPF is converted into a false stereo by a pseudo stereo circuit (3).

Two mutually correlating signals are output by the pseudo stereo circuit. The output of the HPF is added to the two output signals from the stereo circuit by using a pair of adders (4A,4B).

ADVANTAGE - Reduces sense of incongruity in stereo sound. Provides sensation of environmental tone and reverberated sound. Obtains compatibility of moving tone.

Dwg.1/14

Derwent Class: W03; W04
International Patent Class (Main): H04S-005/00
International Patent Class (Additional): H04S-001/00; H04S-003/00;
H04S-005/02; H04S-007/00

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Patent Family:
Patent No Kind Date Applic No Kind Date
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PATENT FAMILY:
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FIELD (English)
Patent Assignee: MATSUSHITA ELECTRIC IND CO LTD
Author (Inventor): SUZUKI TATSUYA
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IPC: * H04S-005/00; H04S-001/00; H04S-003/00; H04S-005/02; H04S-007/00
Derwent WPI Acc No: * G 96-352769; G 96-352769
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